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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: **Cannon et al.**

Serial No.: **10/645,038**

Filed: **August 21, 2003**

For: **Apparatus for Functional and
Stress Testing of Exposed Chip Land
Grid Array Devices**

35525

PATENT TRADEMARK OFFICE
CUSTOMER NUMBER

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Group Art Unit: **2829**

Examiner: **Tang, Minh Nhut**

Attorney Docket No.: **AUS920030539US1**

Certificate of Mailing Under 37 C.F.R. § 1.8(a)

I hereby certify this correspondence is being deposited with the
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By:

Amelia C. Turner
Amelia C. Turner

TRANSMITTAL DOCUMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

ENCLOSED HEREWITH:

- Response to Office Action;
- Replacement Sheets for Figures 2 & 7; and
- Our return postcard.

No fees are believed to be required. If, however, any fees are required, I authorize the Commissioner to charge these fees which may be required to IBM Corporation Deposit Account No. 09-0447. No extension of time is believed to be necessary. If, however, an extension of time is required, the extension is requested, and I authorize the Commissioner to charge any fees for this extension to IBM Corporation Deposit Account No. 09-0447.

Respectfully submitted,

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RESPONSE TO OFFICE ACTION

Commissioner for Patents
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In response to the Office Action dated September 22, 2004, please amend the above-identified application as follows: